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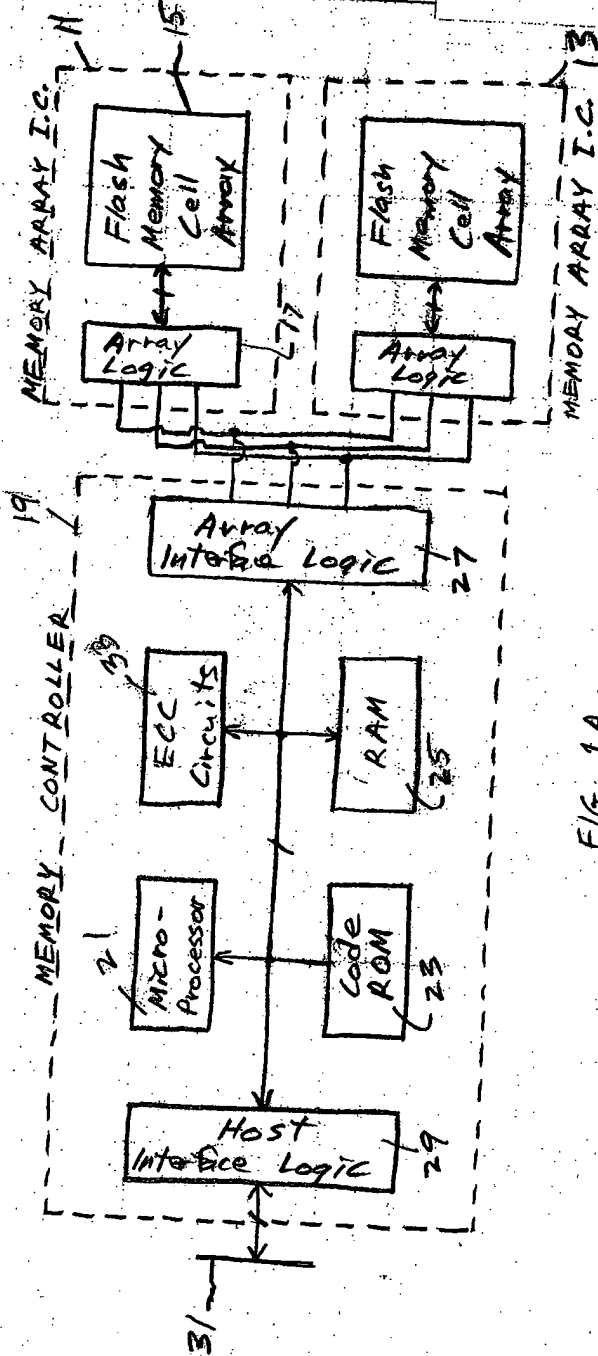


FIG. 1A
Non-Volatile Memory

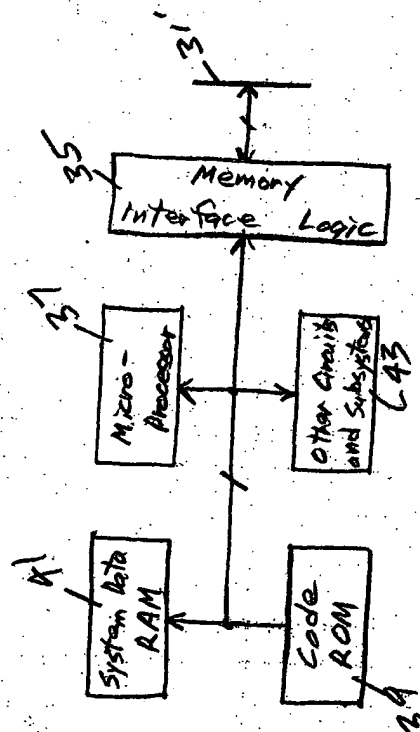


FIG. 1B
Host

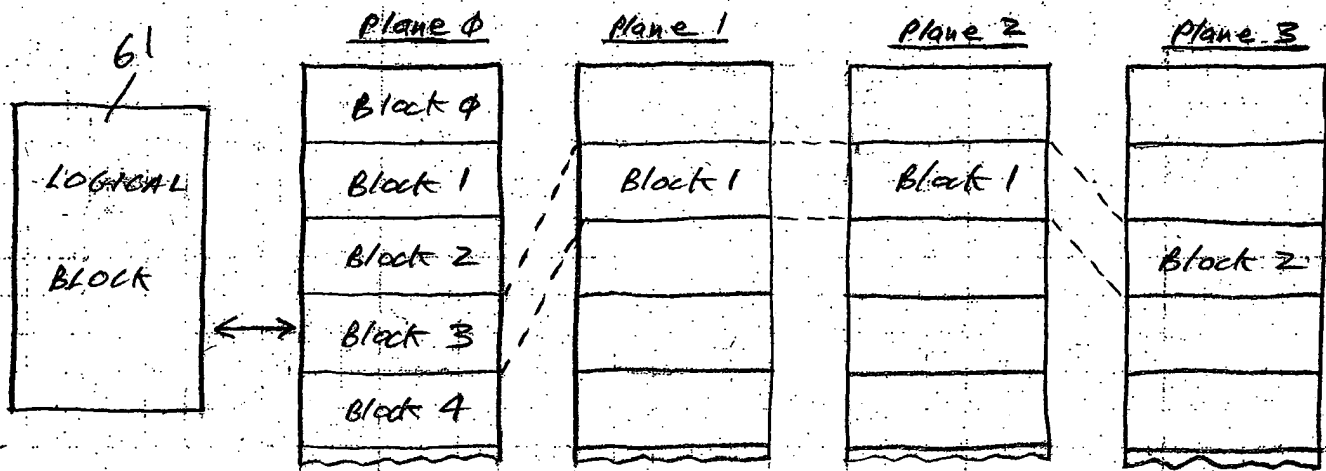


FIG. 6 - Use of Metablocks

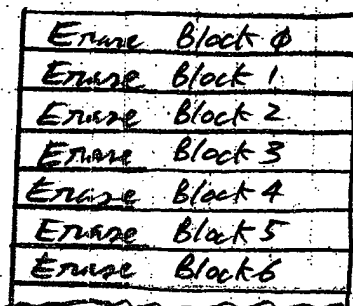


FIG. 2

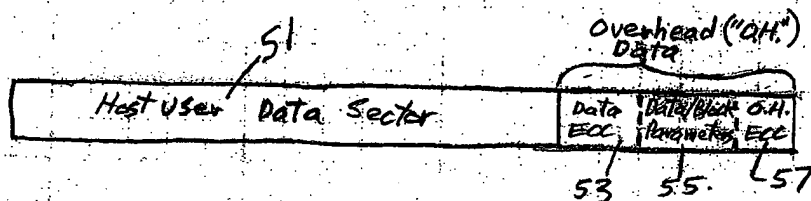


FIG. 3

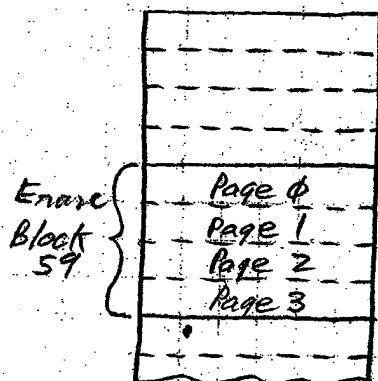


FIG. 4

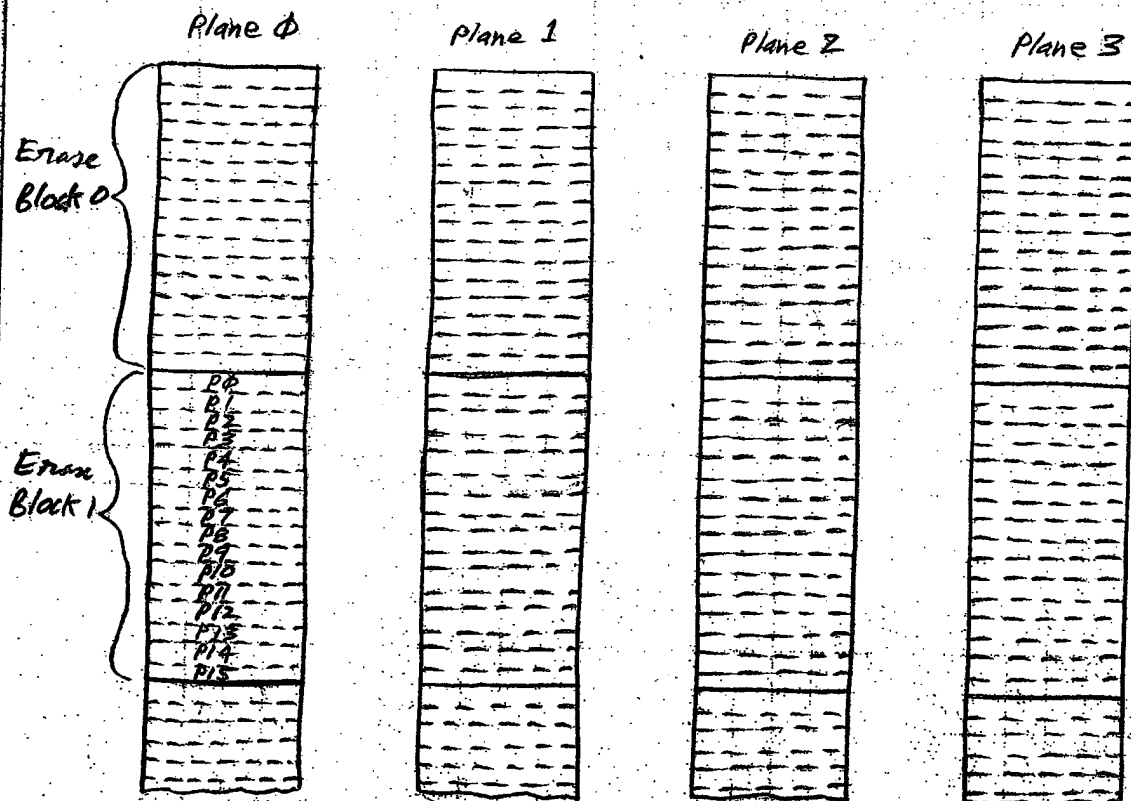


FIG. 5

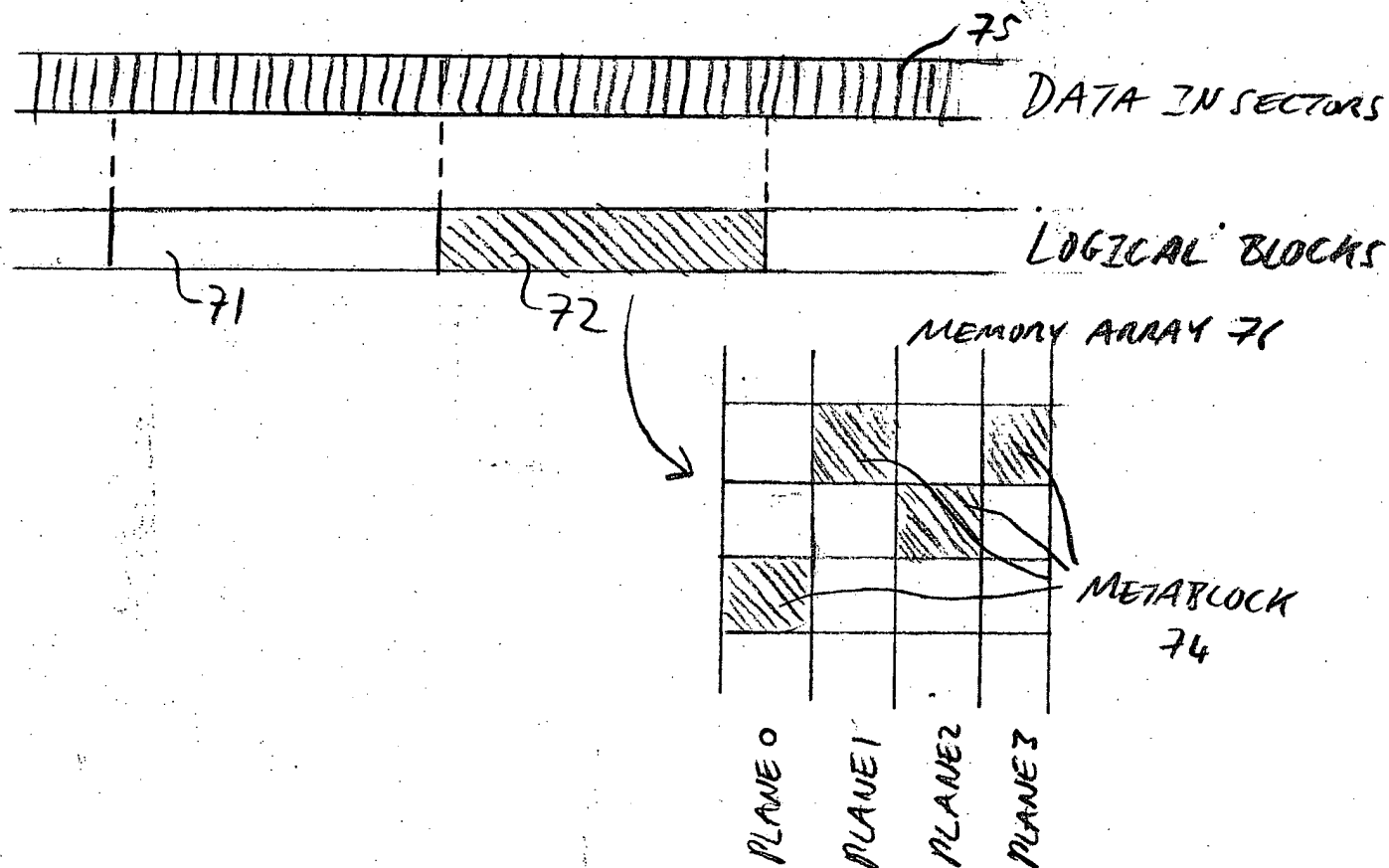


FIGURE 7

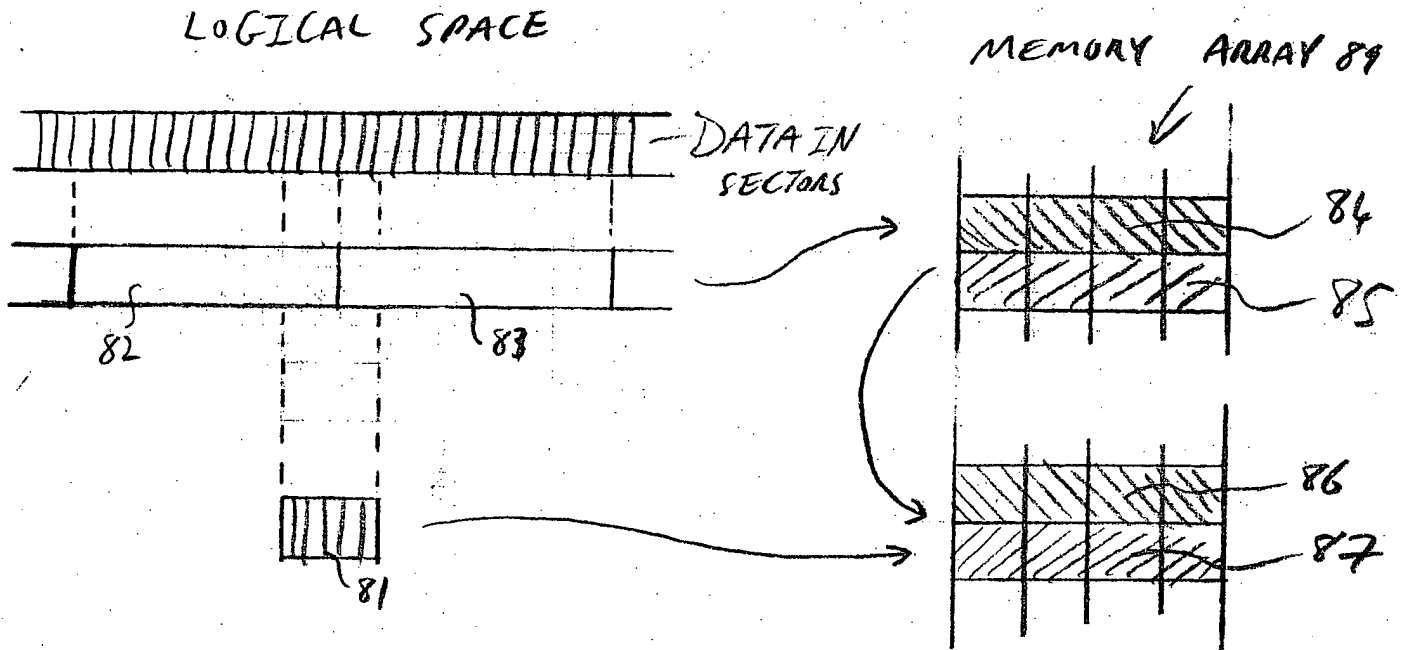


FIGURE 8

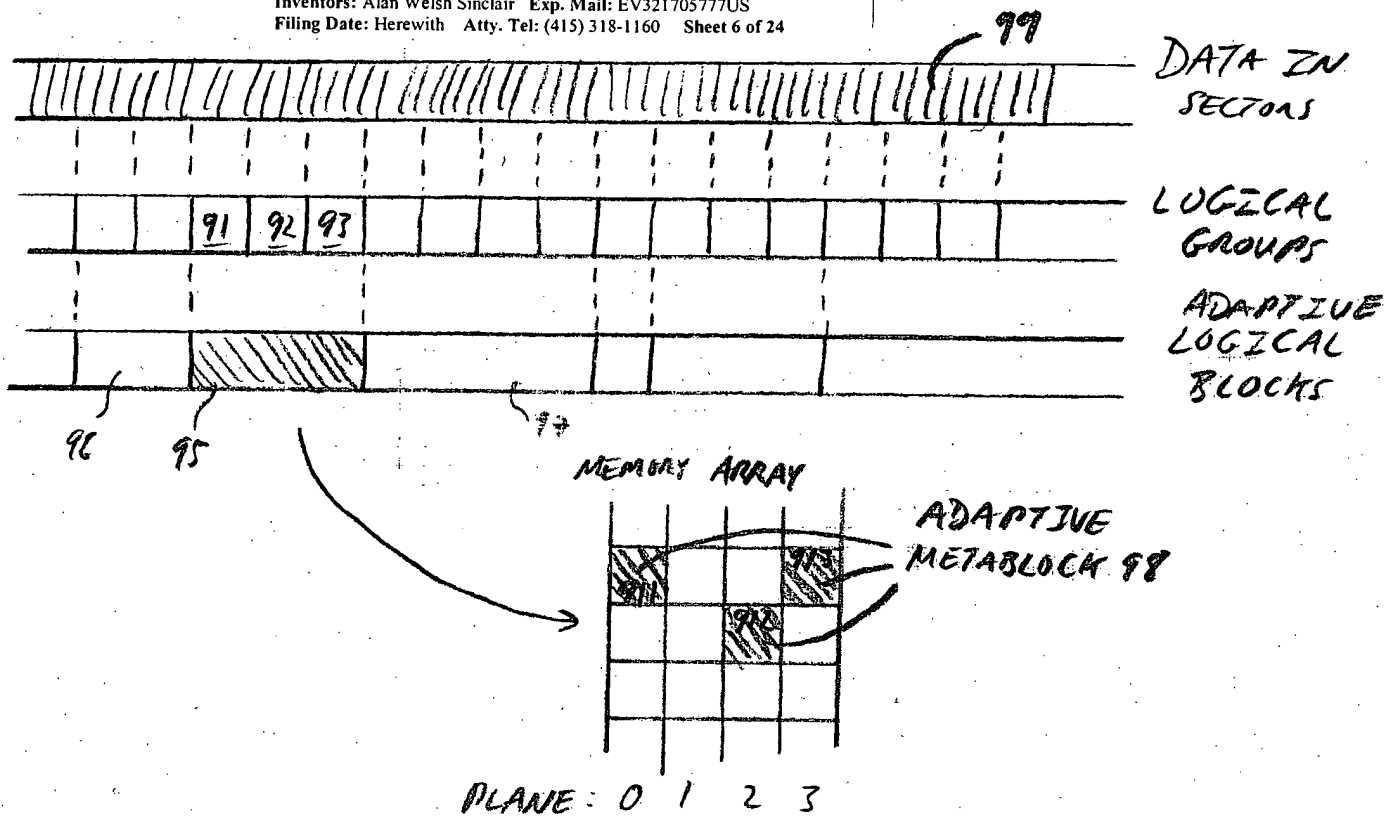


FIGURE 9A

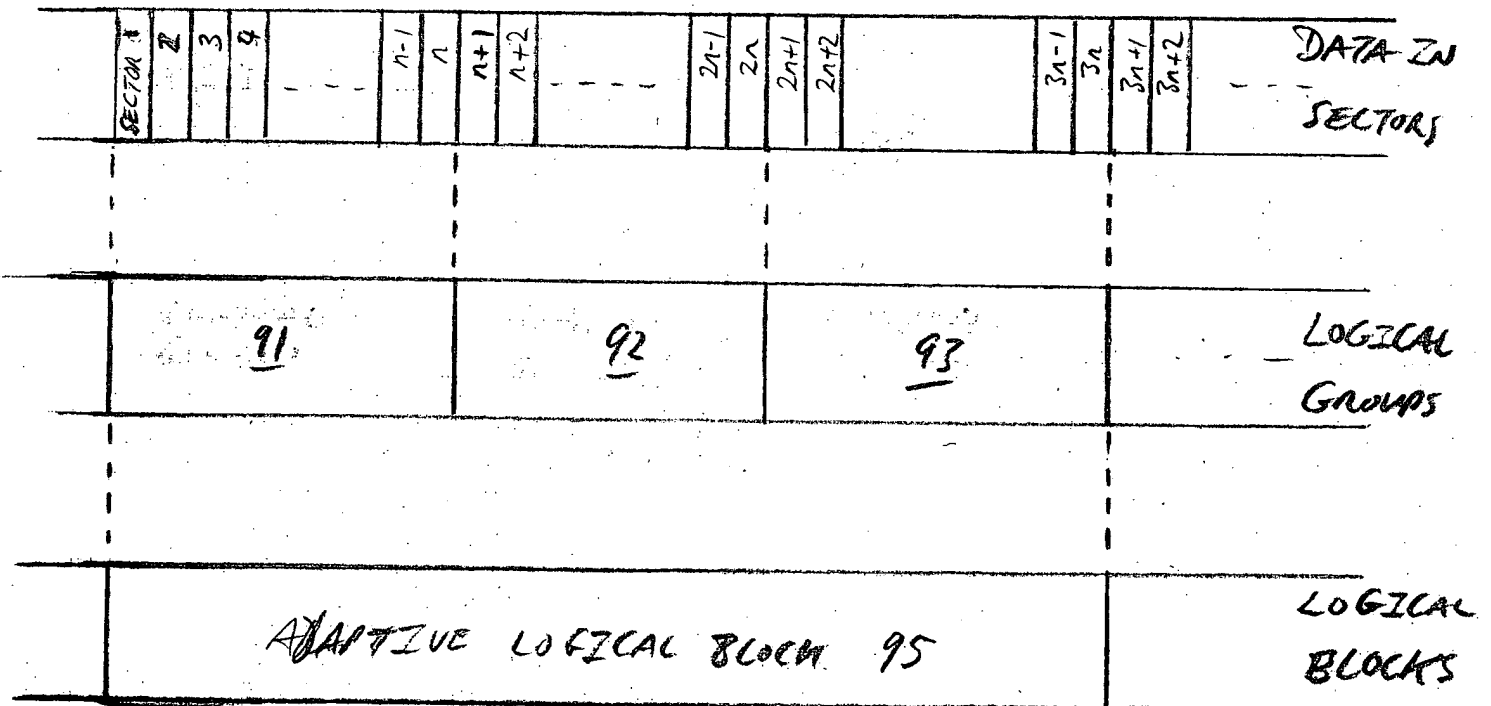


FIGURE 9B

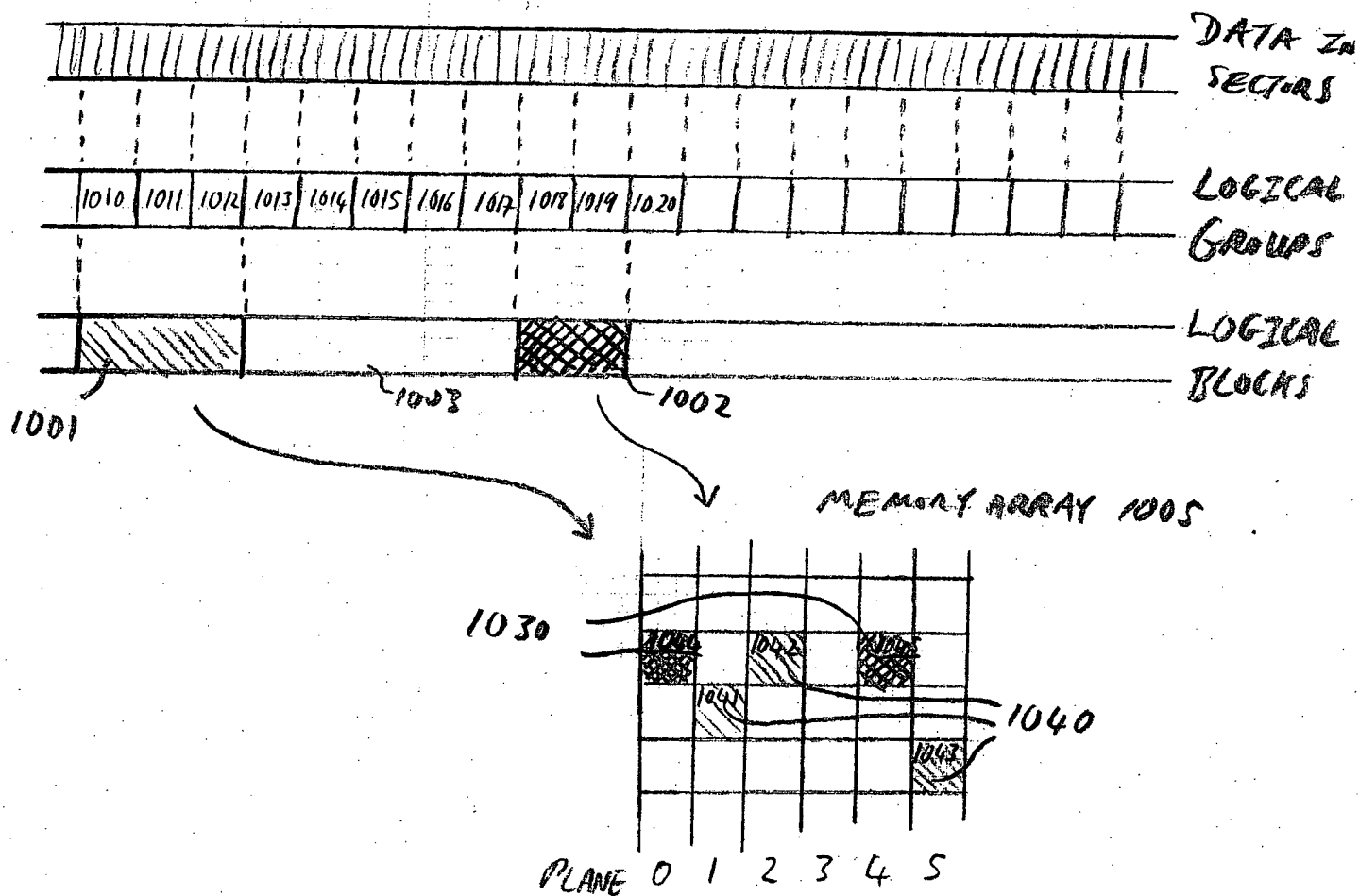


FIGURE 10

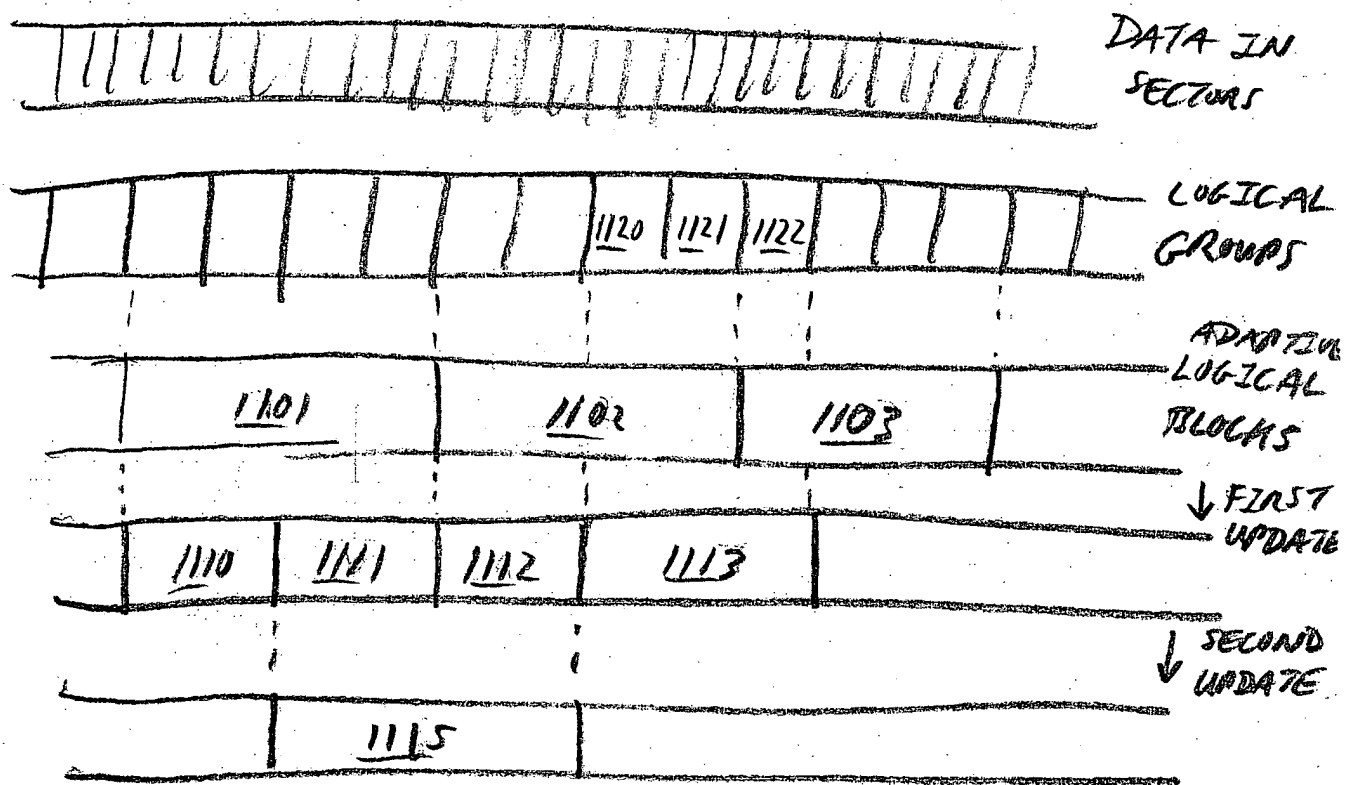
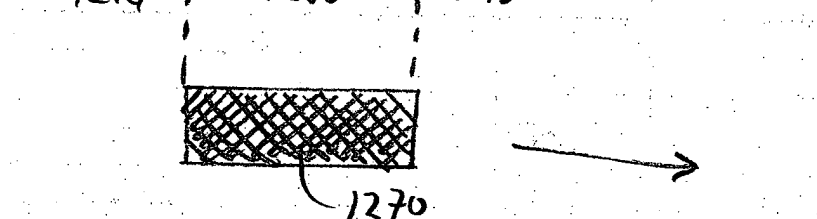
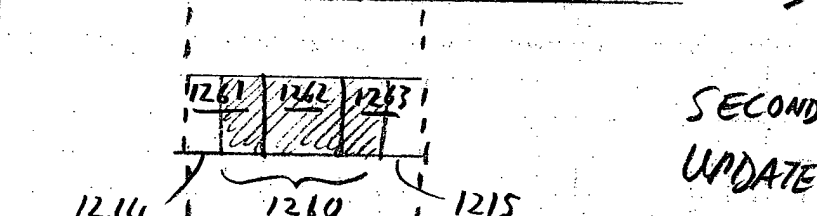
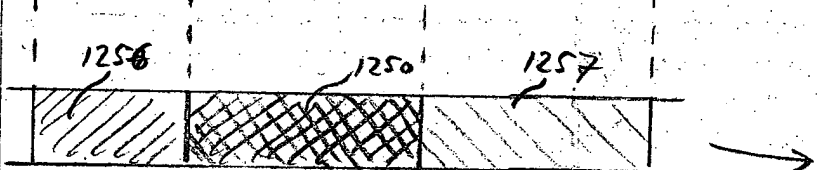
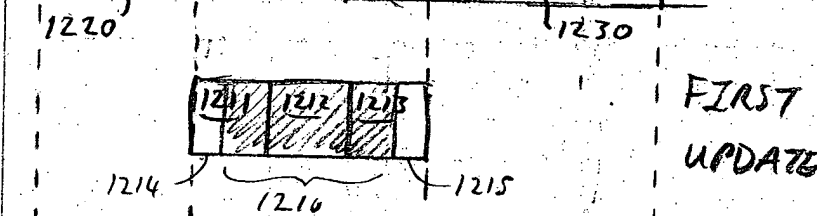
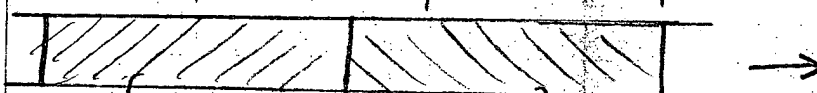
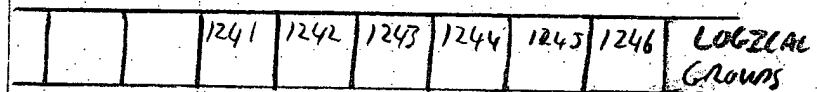
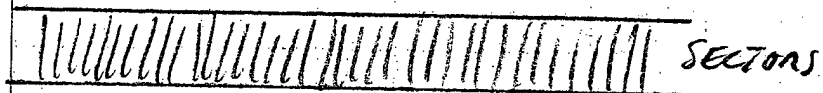


Fig 11

22-141 50 SHEETS
 22-142 100 SHEETS
 22-144 200 SHEETS



LOGICAL ADDRESS SPACE



MEMORY ARRAY

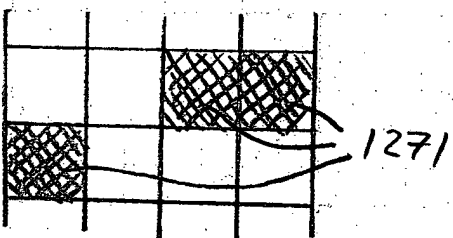
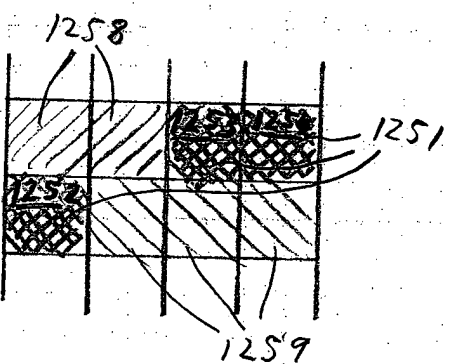
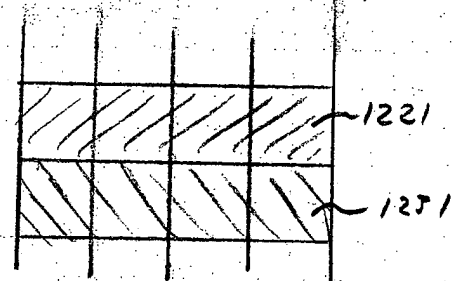


FIGURE 12A

22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

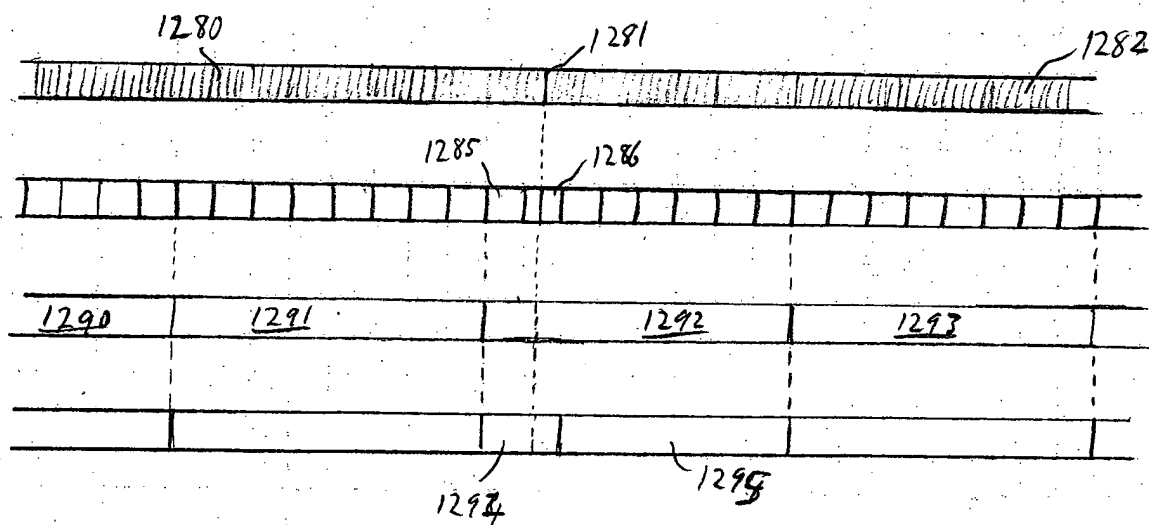


FIGURE 128

22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

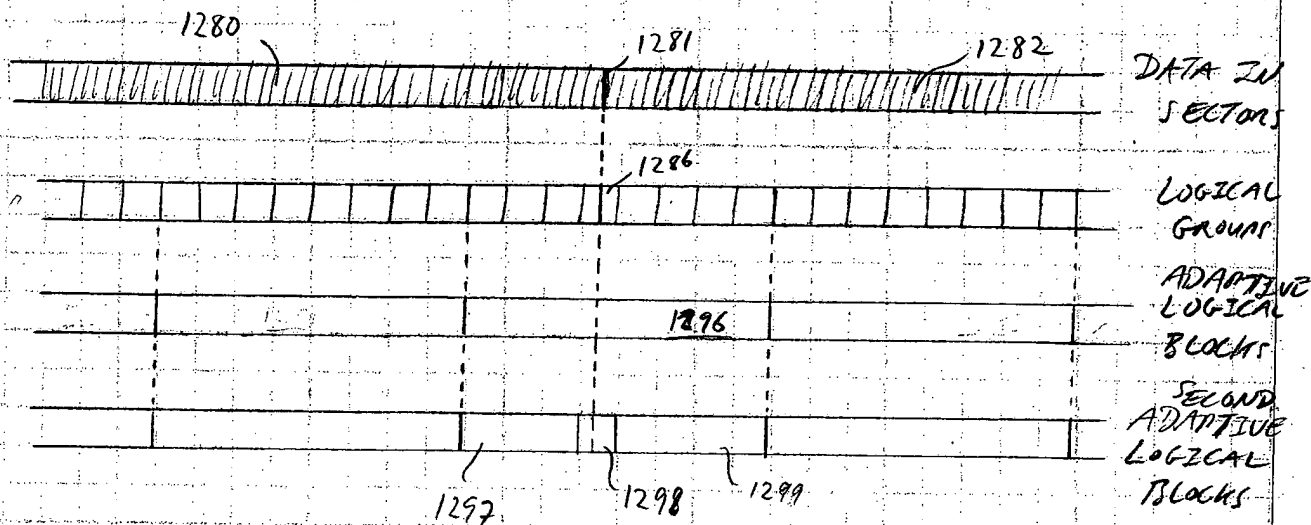


FIGURE 12C

22-141 50 SHEETS
 22-142 100 SHEETS
 22-144 200 SHEETS
 AMPAD

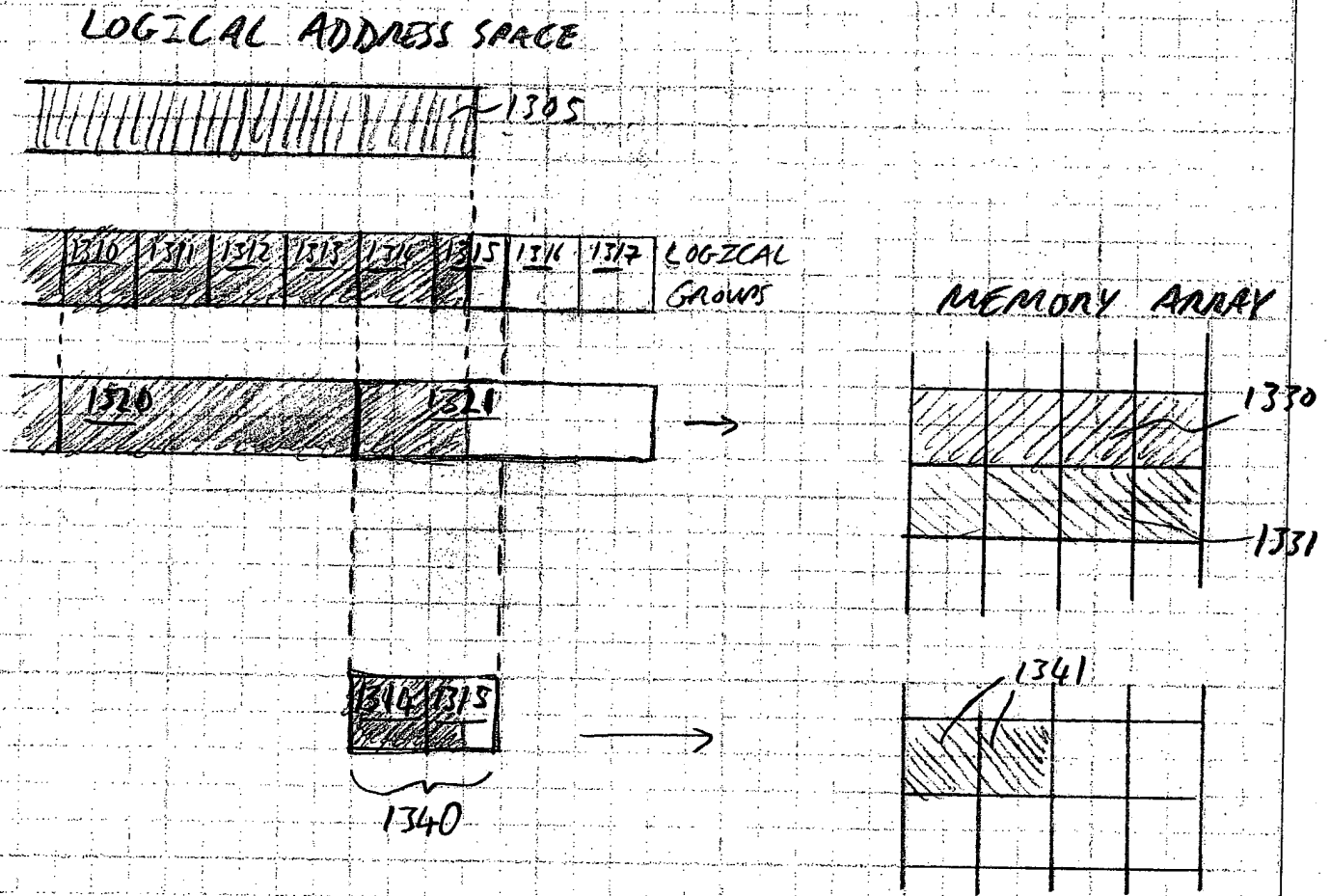


FIGURE 13

22-141 50 SHEETS
 22-142 100 SHEETS
 22-144 200 SHEETS

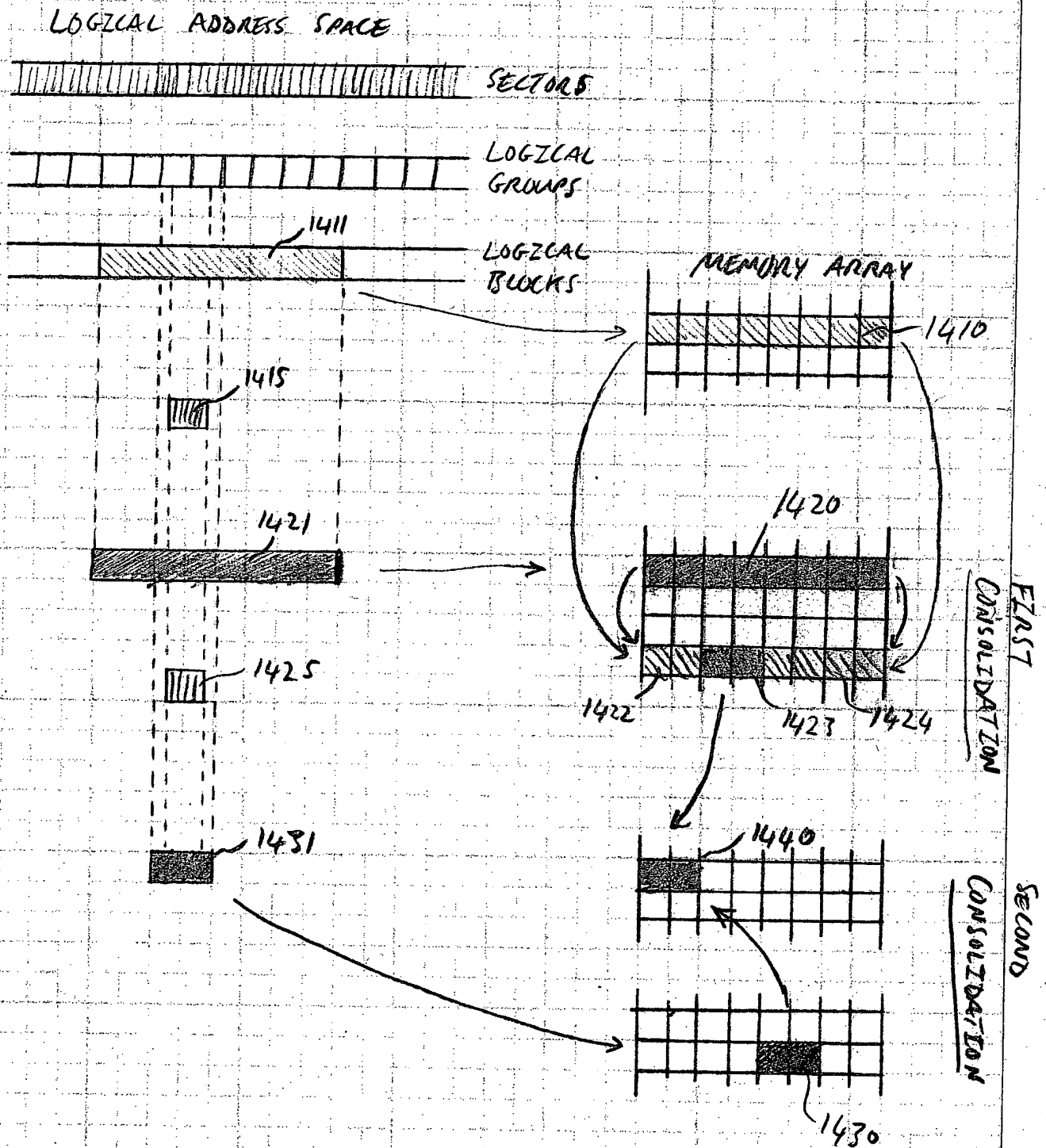
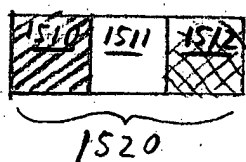


FIGURE 14



MEMORY ARRAY

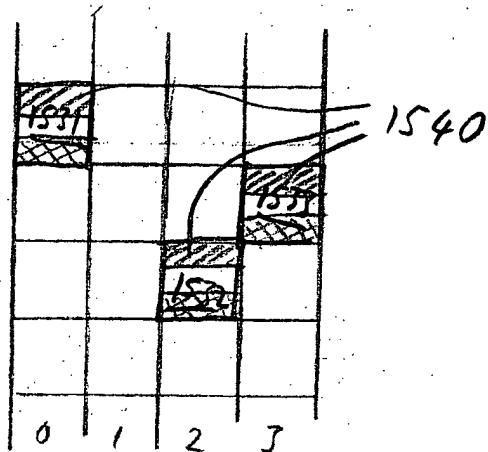


FIGURE 15A

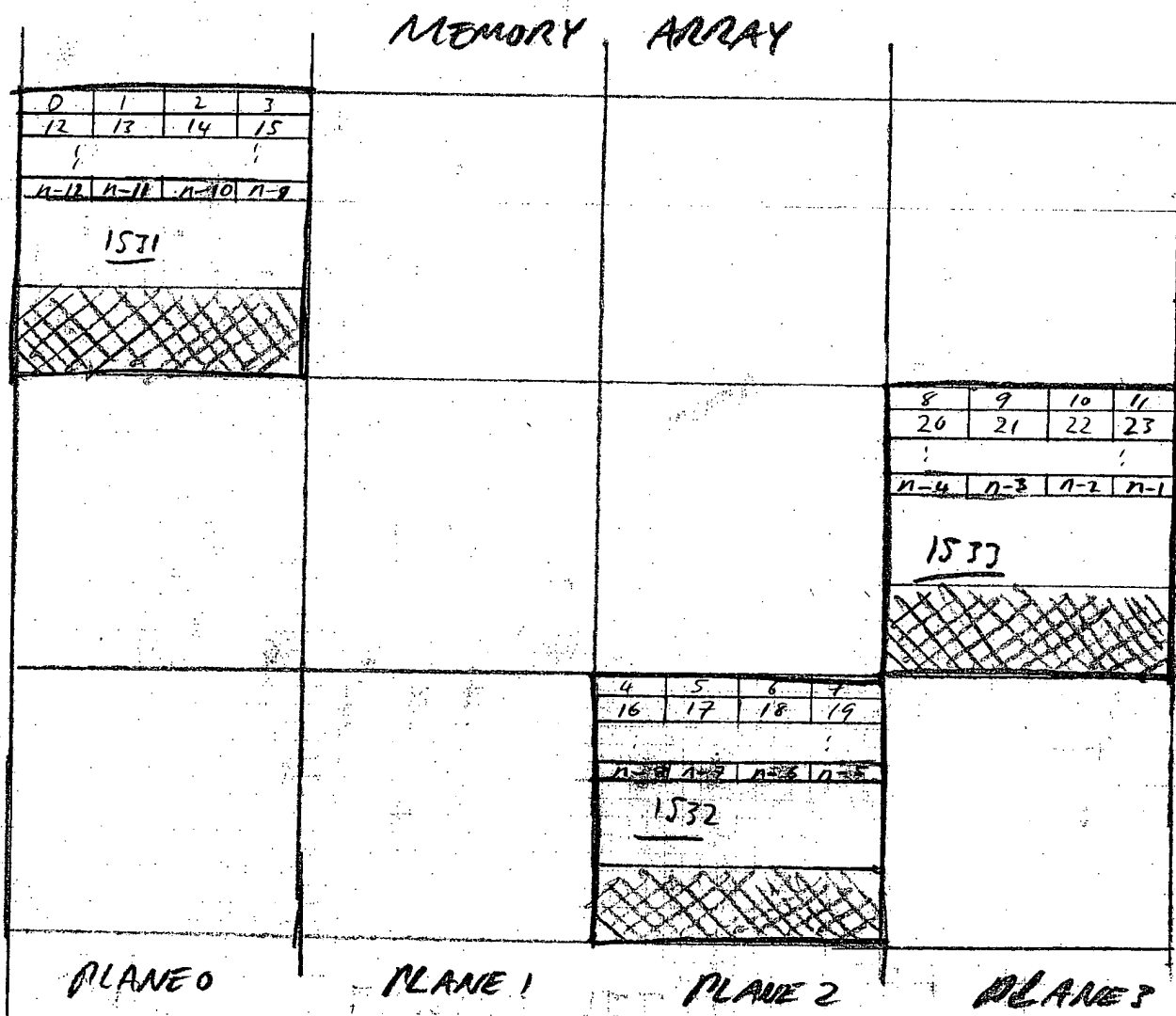


FIGURE 15B

MEMORY ARRAY

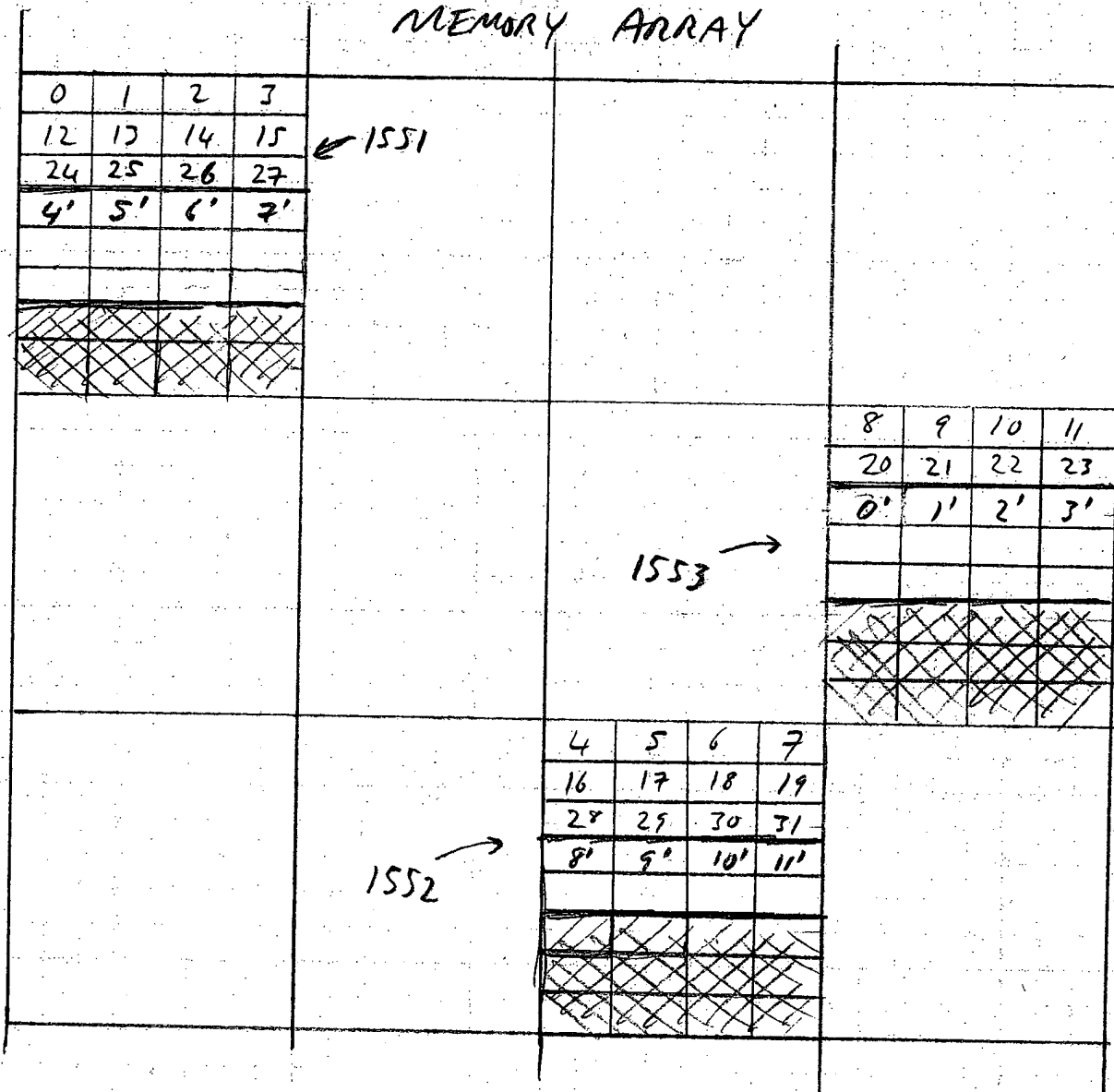


FIGURE 15C

MEMORY ARRAY

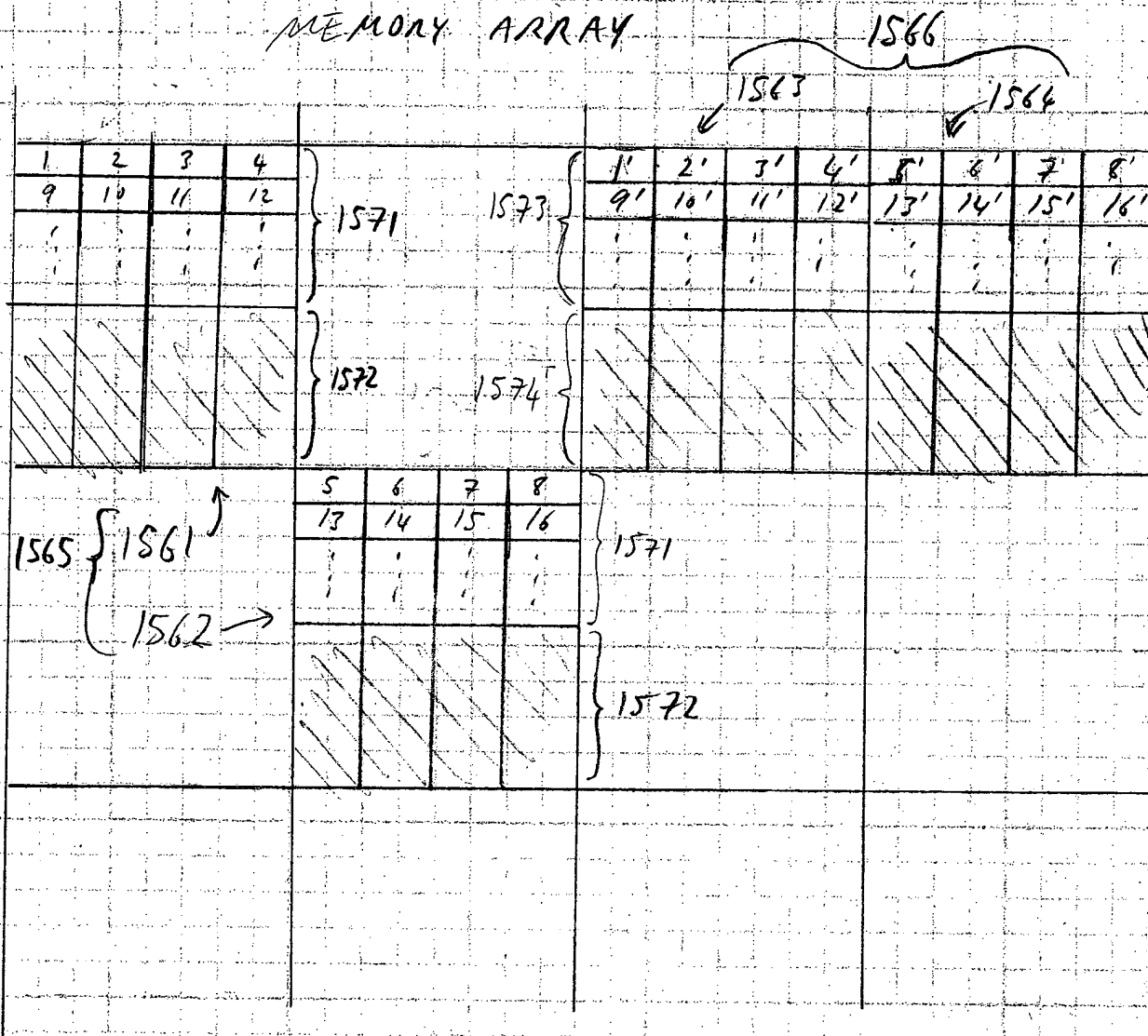


FIGURE 15D

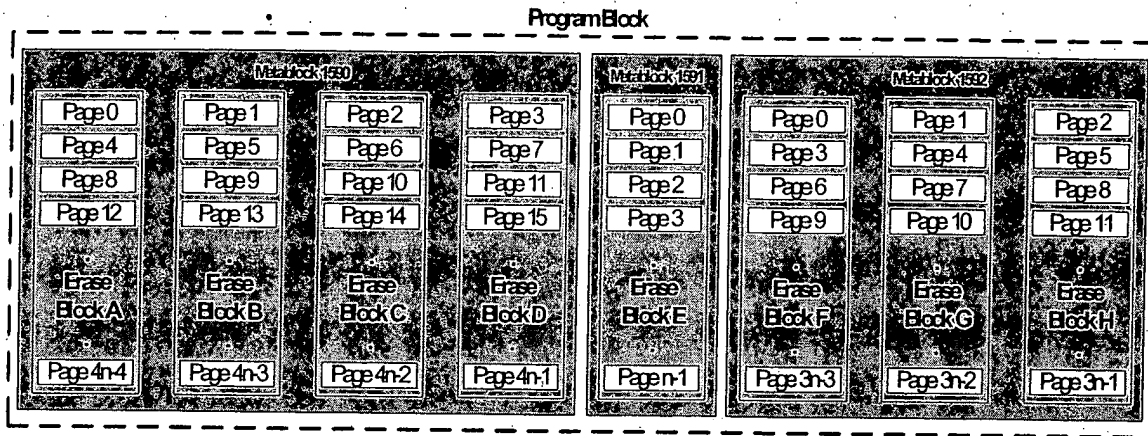
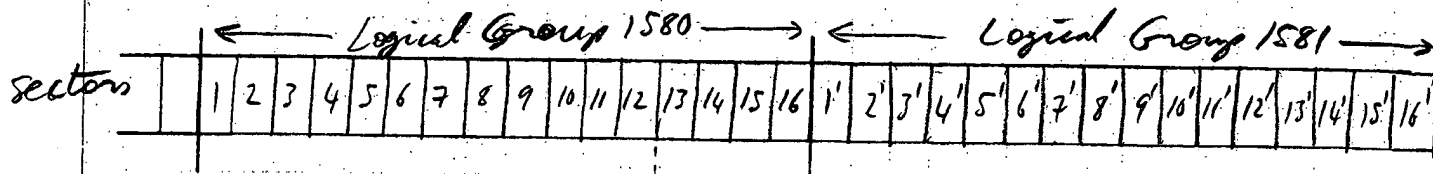


FIGURE 15E



UPDATED
DATA
1582



MEMORY ARRAY

1585 →

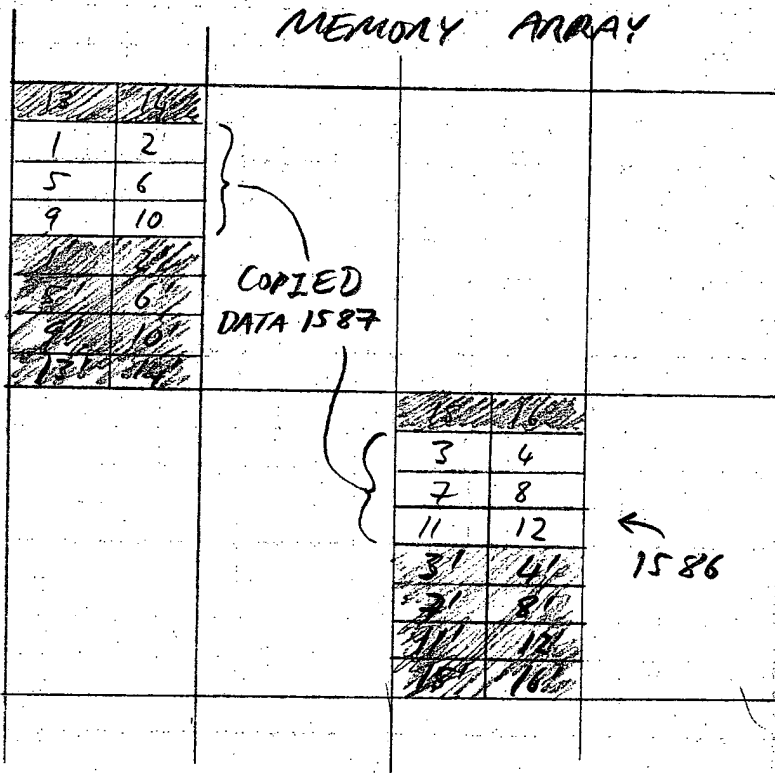


FIGURE 15F

LOGICAL GROUP	SIZE	LOGICAL GROUP No.	BLOCK
L1	3	1	A
L2	3	2	B
L3	3	3	C

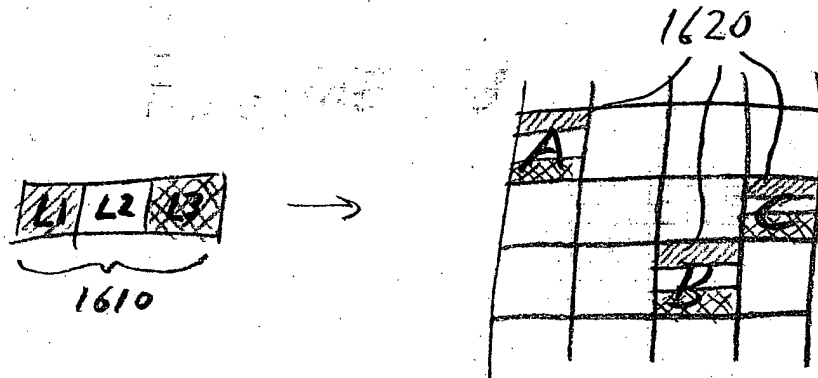


FIGURE 16

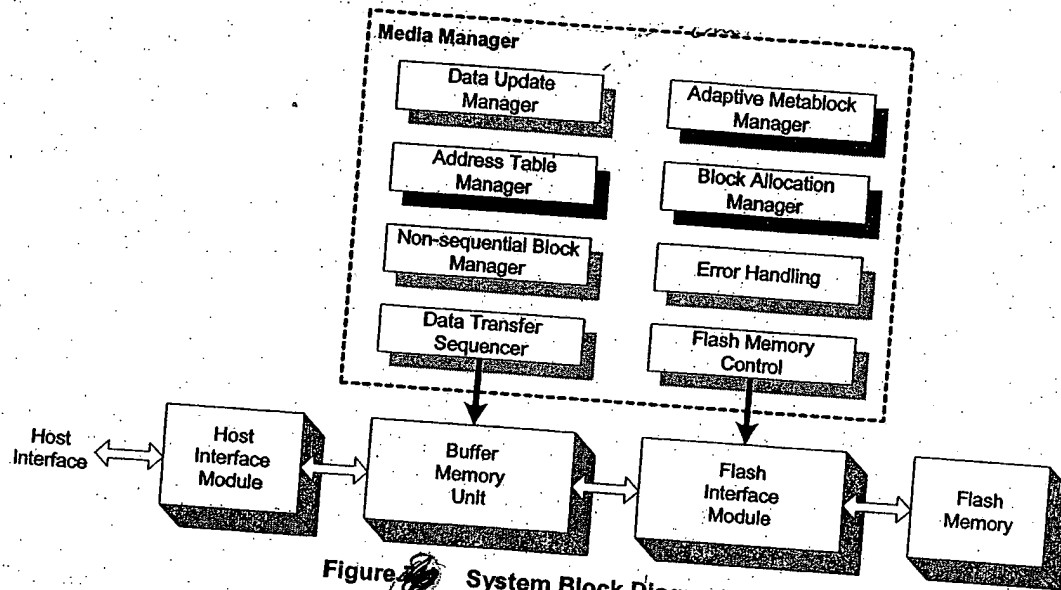


Figure 17 System Block Diagram

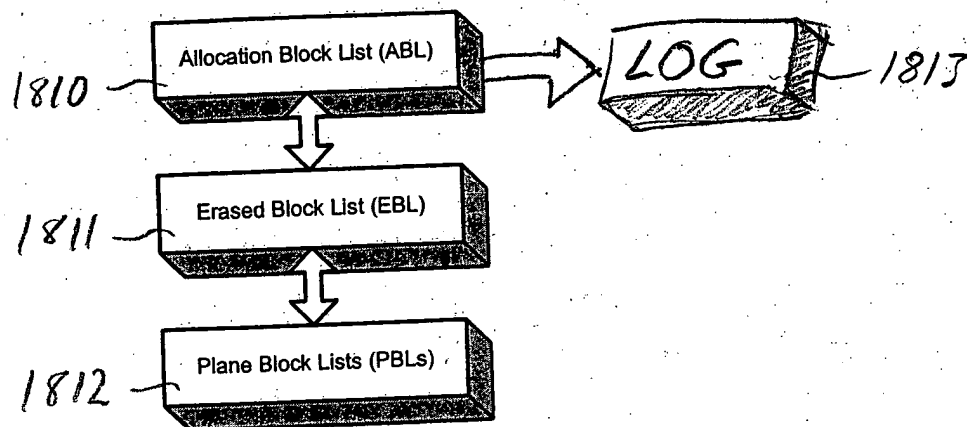


FIGURE 18A ERASED BLOCK MANAGEMENT HIERARCHY

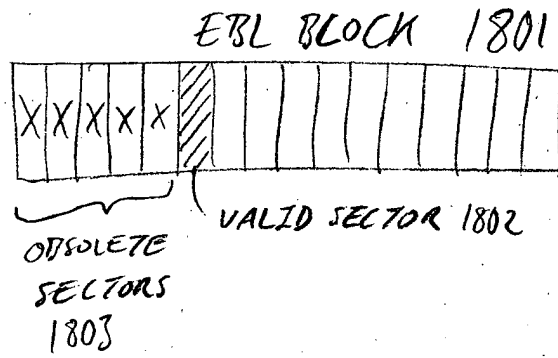


FIGURE 188

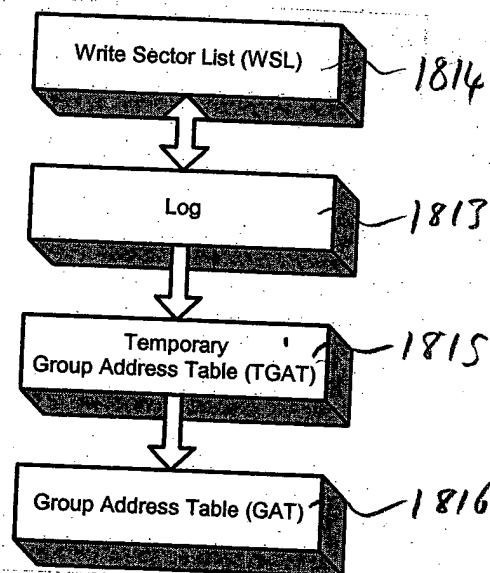


FIGURE 18 ADDRESS TABLE MANAGEMENT HIERARCHY

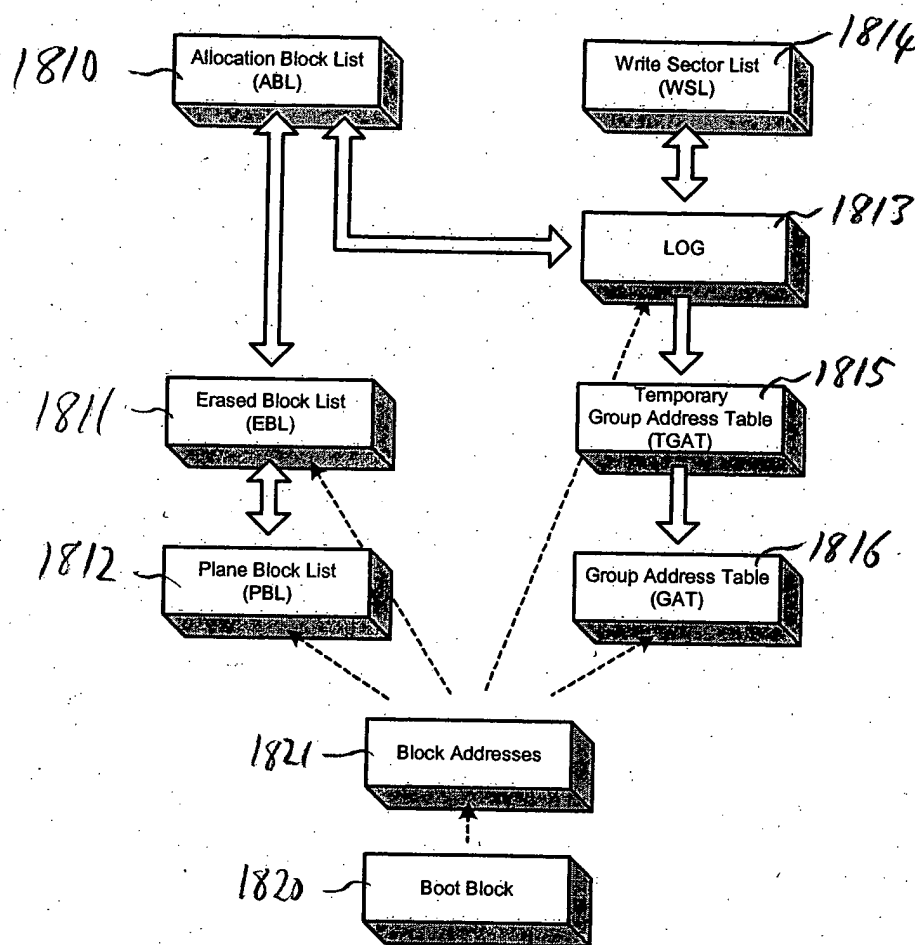


FIGURE 18D DATA STRUCTURE REFERENCE TREE